

JCE68 U.S. PTO

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AN

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10036388	01/07/2002	257		2814	

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Sugihara Kohei

****CONTINUING DATA VERIFIED:**

none, pc

**** FOREIGN APPLICATIONS VERIFIED:**

JAPAN 2001-178107 06/13/2001

yes, pc

PG-PUB DO NOT PUBLISH ☐RESCIND ☐

Foreign priority claimed

☒ yes ☐ no

35 USC 119 conditions met

☒ yes ☐ no

Verified and Acknowledged Examiners initials

PC

ATTORNEY DOCKET NO

740819-710

TITLE : Semiconductor device and method for fabricating the same

U.S. DEPT. OF COM. PAT. & TM. FPO 436 Rev. 12/94

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
Primary Examiner		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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